

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Wilbur Catabay et. al.



Serial No.: 09/884,736

Filed : June 19, 2001

For : In-Situ Plasma Treatment of Low-k films to generate a non-homogenous film for use as an etch stop and or Hardmask in Damascene structures

Atty Docket : / 00-654

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington D.C. 20231, on the date indicated below:

Connie del Castillo

Mark Salvatore

12/19/01 Connie del Castillo

Date Signature

CERTIFICATION UNDER 37 C.F.R. 3.73(b)

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

LSI Logic Corporation, a Delaware corporation, certifies that it is the assignee of the entire right, title and interest in the patent application.

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of the undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered to act on behalf of the assignee.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

LSI Logic Corporation
1551 McCarthy Blvd., MS D-106
Milipitas, CA 95035
(408) 433-8708

Date: 12/18/01

Respectfully submitted,

S. Jaggi
Sandeep Jaggi
Chief IP Counsel
Corporate Assistant Secretary
LSI LOGIC CORPORATION